

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Michael S. Briner	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	703.007US8
Title:	READ-BIASING AND AMPLIFYING SYSTEM		

INFORMATION DISCLOSURE STATEMENT

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicant respectfully requests that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicant requests that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicant with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement.

Pursuant to 37 C.F.R. §1.98(d), copies of the listed documents are not provided as these references were previously cited by or submitted to the U.S. Patent Office in connection with Applicant's prior U.S. application, Serial No. 10/352431, filed on January 28, 2003, which is relied upon for an earlier filing date under 35 U.S.C. §120.

The Examiner is invited to contact the Applicant's Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

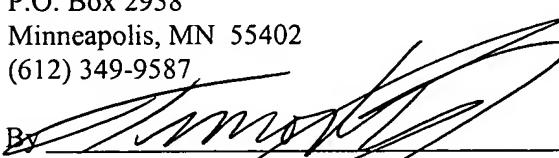
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Date

25 Feb '04

By 
Timothy B Clise
Reg. No. 40,957

"Express Mail" mailing label number: EV370240023US

Date of Deposit: February 25, 2004

This paper or fee is being deposited on the date indicated above with the United States Postal Service pursuant to 37 CFR 1.10, and is addressed to The Commissioner for Patents, Mail Stop Patent Application, P.O.Box 1450, Alexandria, VA 22313-1450.

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	Unknown
		Filing Date	Even Date Herewith
		First Named Inventor	Briner, Michael
		Group Art Unit	Unknown
		Examiner Name	Unknown
Sheet 1 of 4		Attorney Docket No: 703.007US8	

US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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	US-4,250,570	02/10/1981	Tsang, Frederick , et al.	365	200	01/09/1978
	US-4,389,715	06/21/1983	Eaton, Sargent S., et al.	365	200	10/06/1980
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DATE CONSIDERED

INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known				
		Application Number		Unknown		
		Filing Date		Even Date Herewith		
		First Named Inventor		Briner, Michael		
		Group Art Unit		Unknown		
		Examiner Name		Unknown		
Sheet 2 of 4		Attorney Docket No: 703.007US8				

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EXAMINER

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known	
		Application Number	Unknown
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		First Named Inventor	Briner, Michael
		Group Art Unit	Unknown
		Examiner Name	Unknown
Sheet 3 of 4		Attorney Docket No: 703.007US8	

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Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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EXAMINER

DATE CONSIDERED

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>		Complete if Known Application Number Unknown Filing Date Even Date Herewith First Named Inventor Briner, Michael Group Art Unit Unknown Examiner Name Unknown	
Sheet 4 of 4		Attorney Docket No: 703.007US8	

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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
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EXAMINER**DATE CONSIDERED**

S/N Unknown

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Michael S. Briner	Examiner:	Unknown
Serial No.:	Unknown	Group Art Unit:	Unknown
Filed:	Herewith	Docket:	703.007US8
Title:	READ-BIASING AND AMPLIFYING SYSTEM		

COMMUNICATION CONCERNING RELATED APPLICATION(S)

MS Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Applicant would like to bring to the Examiner's attention the following related application(s) in the above-identified patent application:

<u>Serial/Patent No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>	<u>Title</u>
08/386704 5526364	February 10, 1995	703.004US1	APPARATUS FOR ENTERING AND EXECUTING TEST MODE OPERATIONS FOR MEMORY
08/387018 5627786	February 10, 1995	703.005US1	PARALLEL PROCESSING REDUNDANCY SCHEME FOR FASTER ACCESS TIMES AND LOWER DIE AREA
08/826241 5,808,946	March 27, 1997	703.005US2	PARALLEL PROCESSING REDUNDANCY SCHEME FOR FASTER ACCESS TIMES AND LOWER DIE AREA
08/804,951 5,862,077	Feb. 24, 1997	703.007US1	FAST SENSING AMPLIFIER FOR FLASH MEMORY
08/895,618 5,835,411	July 17, 1997	703.007US2	Computer including a fast sensing amplifier
08/893,282 5,757,697	July 15, 1997	703.007US3	Method for sensing the binary state of a floating-gate memory device

09/136,909 6,108,237	Aug. 20, 1998	703.007US4	FAST SENSING AMPLIFIER FOR FLASH MEMORY
09/642953 6,525,967	August 21, 2000	703.007US5	FAST SENSING AMPLIFIER FOR FLASH MEMORY
08/386688 5,682,496	February 10, 1995	703.008US1	FILTERED SERIAL EVENT CONTROLLED COMMAND PORT FOR FLASH MEMORY
08/908242 6578124	August 7, 1997	703.008US2	SERIAL COMMAND PORT METHOD, CIRCUIT, AND SYSTEM INCLUDING MAIN AND COMMAND CLOCK GENERATORS TO FILTER SIGNALS OF LESS THAN A PREDETERMINED DURATION
09/401886 6581146	September 23, 1999	703.008US3	SERIAL COMMAND PORT METHOD, CIRCUIT, AND SYSTEM INCLUDING MAIN AND COMMAND CLOCK GENERATORS TO FILTER SIGNALS OF LESS THAN A PREDETERMINED DURATION
10/352419	January 28, 2003	703.007US6	INTEGRATED CIRCUIT WITH FEEDBACK
10/352431	January 28, 2003	703.007US7	FEEDBACK BIASING INTEGRATED CIRCUIT

Respectfully submitted,
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Date of Deposit: February 25, 2004

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